



Dunlop 13-6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

5 Applicant(s): Dunlop et al.
Case: 13-6
Serial No.: 09/716,977
Filing Date: November 20, 2000
Group: 2634
10 Examiner: Eva Y. Zheng

Title: Gated Clock Recovery Circuit

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. 1450, Alexandria, VA 22313-1450

Signature: Eva Y. Zheng Date: September 30, 2004

15 AMENDMENT AFTER FINAL REJECTION
UNDER 37 CFR §1.116

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Commissioner for Patents
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Sir:

25 In response to the outstanding final Office Action, dated July 30, 2004, please
amend the above-identified patent application as follows.

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